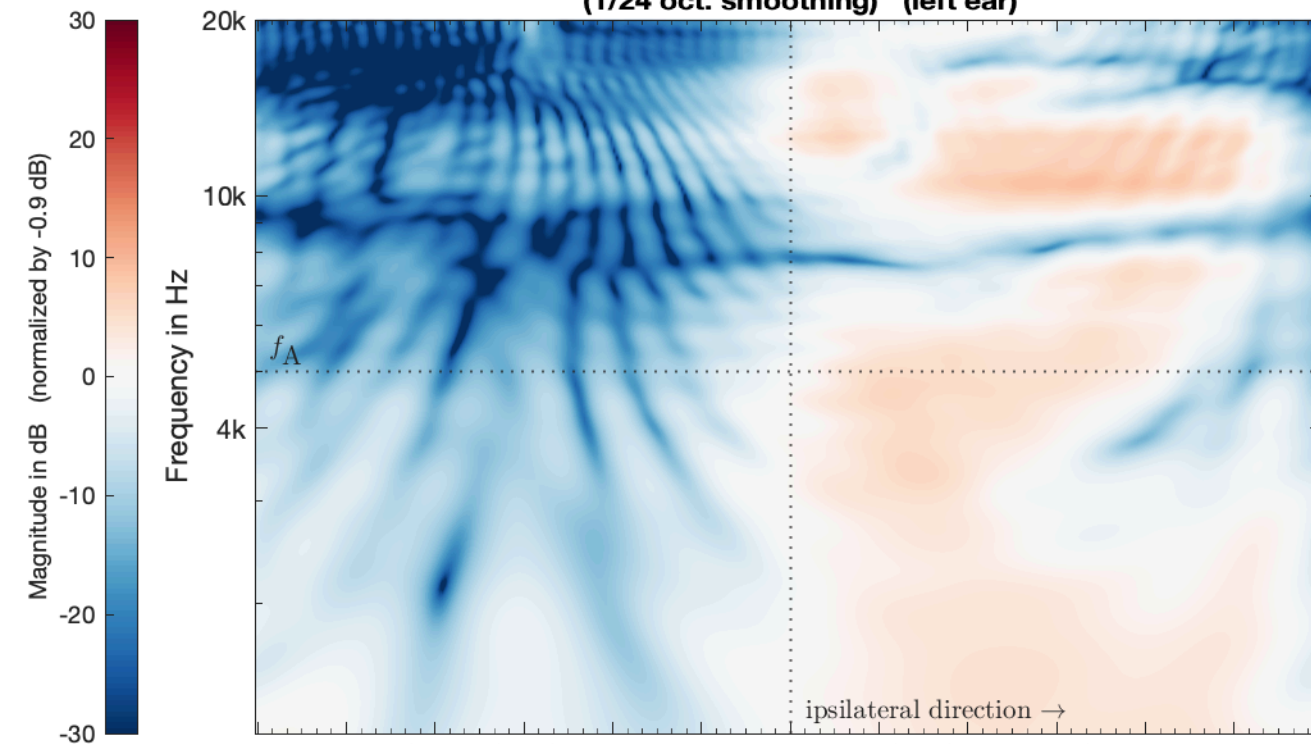
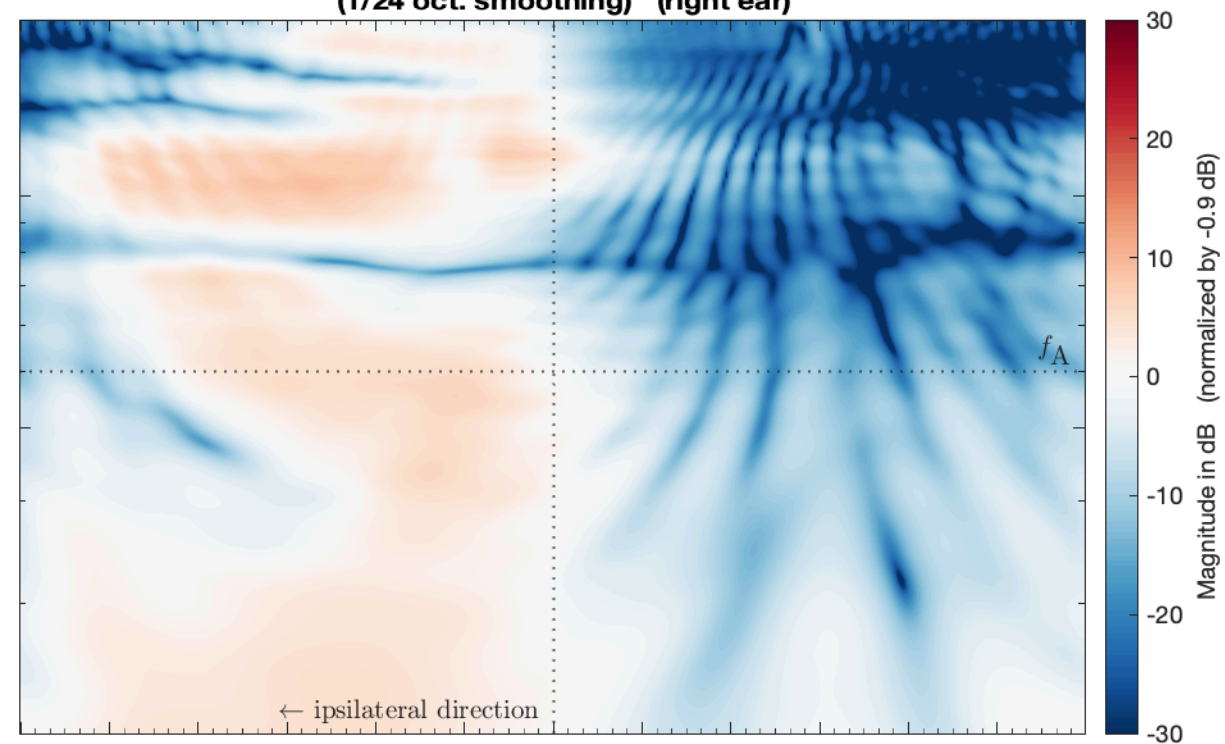


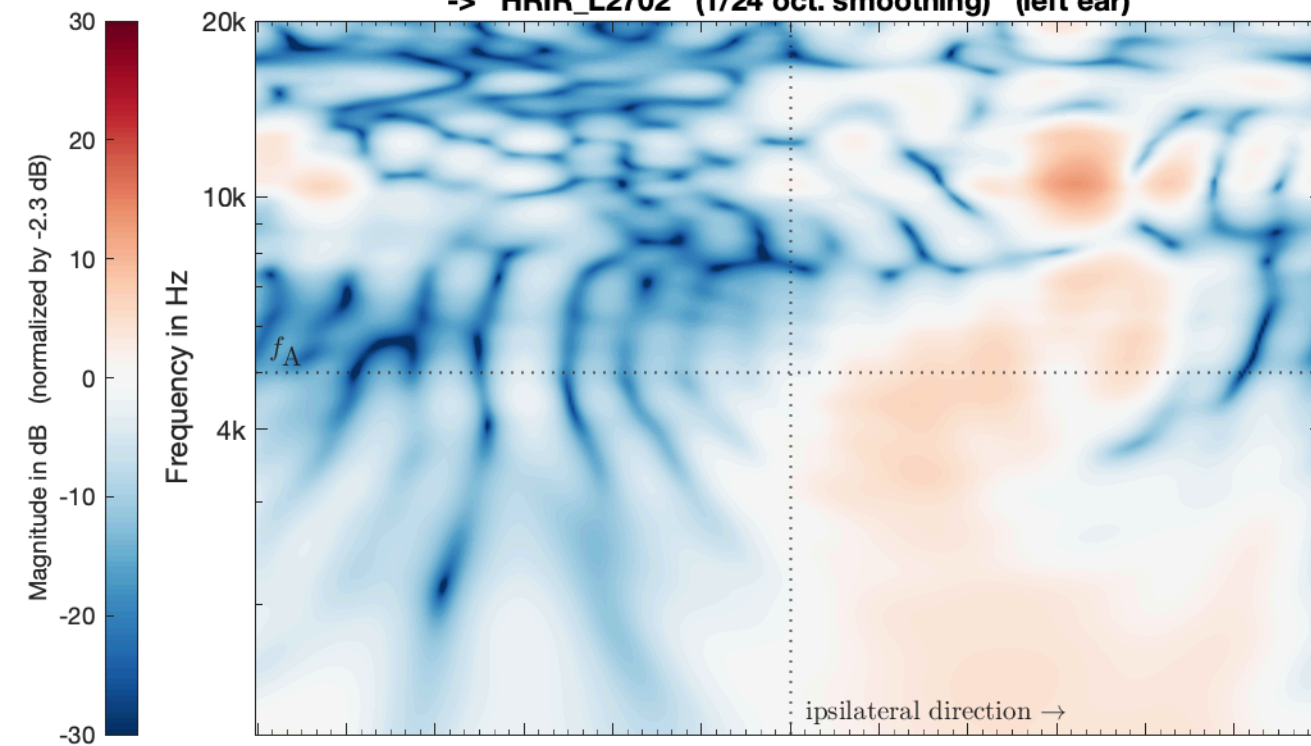
**SIM\_VSA\_SMA\_LE1202\_PW\_struct\_SubSampled\_SH44\_SSR**  
(1/24 oct. smoothing) (left ear)



**SIM\_VSA\_SMA\_LE1202\_PW\_struct\_SubSampled\_SH44\_SSR**  
(1/24 oct. smoothing) (right ear)



**SIM\_VSA\_SMA\_TD146\_PW\_struct\_SubSampled\_SH44**  
-> HRIR\_L2702 (1/24 oct. smoothing) (left ear)



**SIM\_VSA\_SMA\_TD146\_PW\_struct\_SubSampled\_SH44**  
-> HRIR\_L2702 (1/24 oct. smoothing) (right ear)

